

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

YOR919980325S2

Application Number

Not Yet Assigned

Applicant(s)

Stephen M. GATES et al.

Filing Date

Concurrently Herewith

Group Art Unit

Not Yet Assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PC		5,640,343	06/1997	Gallagher et al.			
PC		5,734,605	03/1998	Zhu et al.			
PU		6,097,625	08/2000	Scheuerlein			
PU		6,180,444	01/2001	Gates et al.			
PU		6,190,998-B1	02/2001	Bruel et al.			
PO		6,242,770	06/2001	Bronner et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PC		M. Bruel "The History, Physics, and Applications of the Smart-Cut ® Process MRS Bulletin, Dec. 1998, pp.35-39
PC		B. Aspar et al., (Elsevier Science B.V.) MicroElectronic Engineering 36 (1997) 233-240 "Basic mechanisms involed in the Smart-Cut ® process"

EXAMINER

PHAT X. CAO

DATE CONSIDERED

8/30/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

YOR919980324-62

Application Number

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Applicant(s)

Stephen M. GATES et al.

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Group Art Unit

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*EXAMINER
INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PC

W. J. Gallagher et al., J. Appl. Phys. 81 (8) 15 April 1997, "Microstructured magnetic tunnel junctions (invited)" pp. 3741-3746.

PC

R. DeJule, "Bringing SDI Wafer Technology to the Mainstream", SEMICONDUCTOR INTERNATIONAL 11/1998 P40

PC

J. Markoff "A Milestone on the Road to Ultrafast Computers" New York Times April 6, 1999

EXAMINER

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8/30/02

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